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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/760,405	01/12/2001	Xiaoning Nie	GR 00 P 1031	1766
24131 7590 05/22/2007 LERNER GREENBERG STEMER LLP P O BOX 2480 HOLLYWOOD, FL 33022-2480			EXAMINER LI, AIMEE J	
			ART UNIT 2183	PAPER NUMBER
			MAIL DATE 05/22/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.



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09760405	1/12/01	NIE, XIAONING	GR 00 P 1031

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ART UNIT	PAPER
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20070518

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Commissioner for Patents

As per the conversations had with Applicant's representatives earlier this month, a supplemental Notice of References Cited PTO-892 is attached with the missing Non-Patent Literature document, Hirata et al.'s "An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads" ©1992 ACM. A copy of the document is also attached.

Since the error was not brought to the Examiner's attention until two months into the statutory three month time period, Applicants will receive only a one month reset of time from the date of mailing of this communication, not a full three month restart from the date of mailing. As such, Applicants' have one month from the mailing date of this communication to respond to the the Office Action mailed 22 February 2007 and the attached supplemental Notice of References Cited PTO-892. For more information regarding this reset of time, please see MPEP 710.06.

Aimee J. Li
18 May 2007